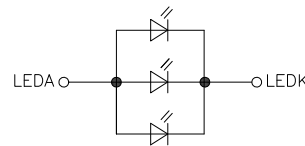


PIN ASSIGNMENT	
PIN	SYMBOL
1	LEDA
2	LEDK
3	IM2
4	IM1
5	IM0
6	VDD
7	DB7
8	DB6
9	DB5
10	DB4
11	DB3
12	DB2
13	DB1
14	DB0
15	RD
16	WR
17	RS
18	CS
19	TE
20	RESET
21	SDA
22	GND




BACKLIGHT CIRCUIT DIAGRAM

NOTES:

1. DISPLAY TYPE: TFT, TRANSMISSIVE, NORMALLY BLACK
2. OPERATING VOLTAGE: VDD=2.8V
3. VIEWING DIRECTION: FULL VIEWING ANGLE
4. IC DRIVER: ST7789V2
5. OPERATING TEMP.: -20°C ~ 70°C
6. STORAGE TEMP.: -30°C ~ 80°C
7. LED BACKLIGHT: 3-CHIP WHITE LED, If=20*3mA, Vf=2.9V~3.1V
8. TOP POLARIZER SURFACE TREATMENT: ANTI-GLARE
9. SURFACE LUMINANCE: 400CD/M²(TYP.)
9. RECOMMEND MATCHING CONNECTOR: FH19SC-22S-0.5SH OR EQUIVALENT
10. THE SINGLE LAYER AREA OF FPC IS BENDABLE, AND THE BENDING RADIUS MUST BE NOT LESS THAN 1.5MM
11. GENERAL TOLERANCE: ±0.2MM
12. ROHS COMPLIANT

03	CORRECT IC DRIVER FROM ST7789V TO ST7789V2.	CAROL	2021.02.24
02	CORRECT THE BACKLIGHT CIRCUIT DIAGRAM	RITA	2020.07.13
01	FIRST ISSUE	RITA	2020.07.10
VER.	REVISED DESCRIPTION	REVISER	DATE

 MULTI-INNO TECHNOLOGY CO., LTD.			
DRAWN BY:	RITA	2020.07.10	MODULE P/N:
CHECKED BY:			MIO2002T-3
APPROVED BY:			DESCRIPTION:
VERSION NO:	03		TFT MODULE
		SCALE : 1/1	
		UNIT: mm	
		SHEET NO.: 1/1	